Hart Hart Hart Hart Hart Hart Hart

VIDEO OUTPUT MONITOR ∞ IMAGE WIDTH DETECTING CIRCUIT UNFOCUSED CONTROL CIRCUIT CAMERA PROCESS CIRCUIT 9 **4**a \mathbf{S} IRIS DRIVING CIRCUIT IRIS CONTROL CIRCUIT CCD 2b \mathfrak{C} 7 FOCUS DRIVING CIRCUIT 2a VAP DRIVING CIRCUIT VAPVIBRATION DETECTING SENSOR

0,

FROM RECORDER

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FIG. 2

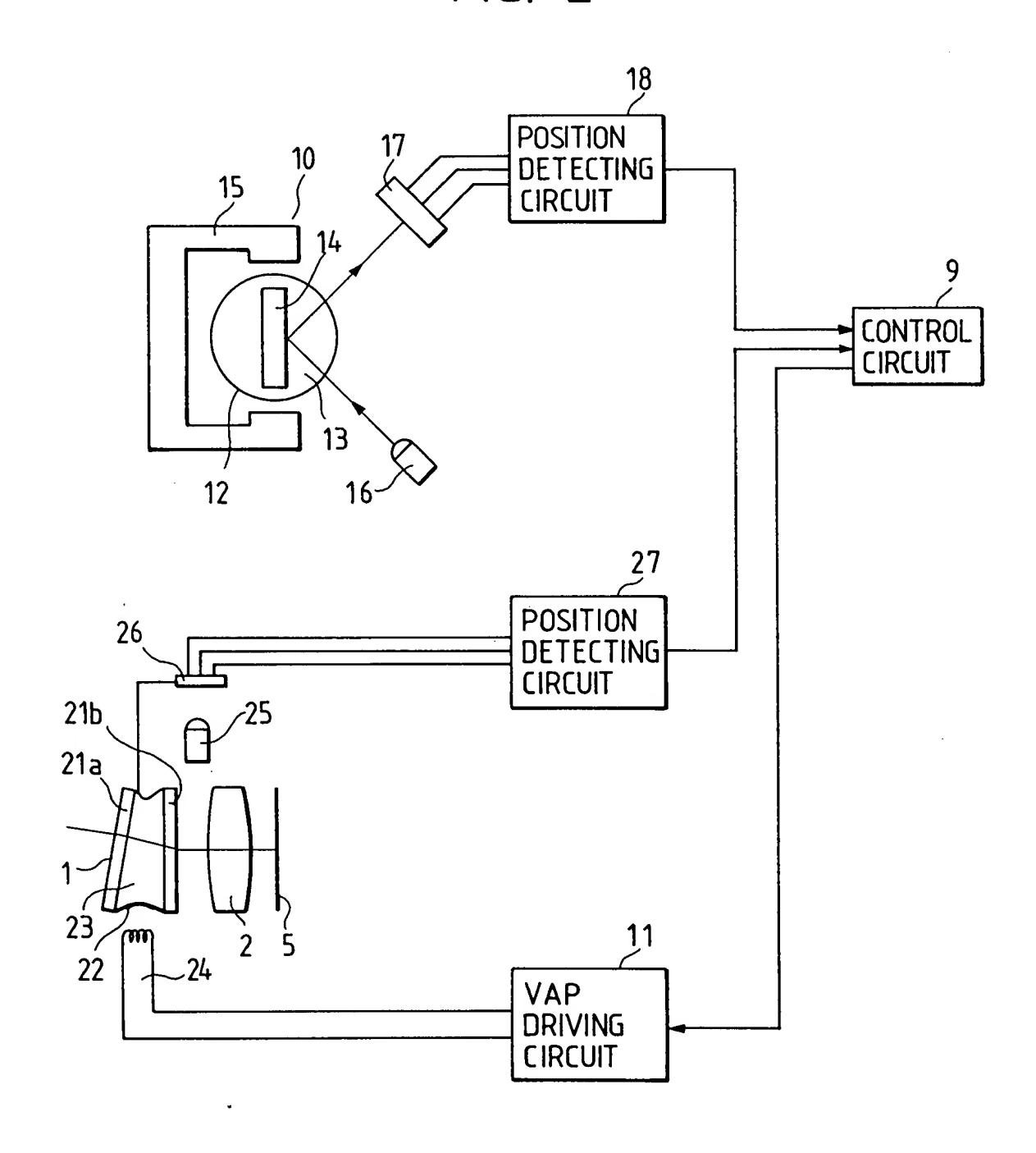
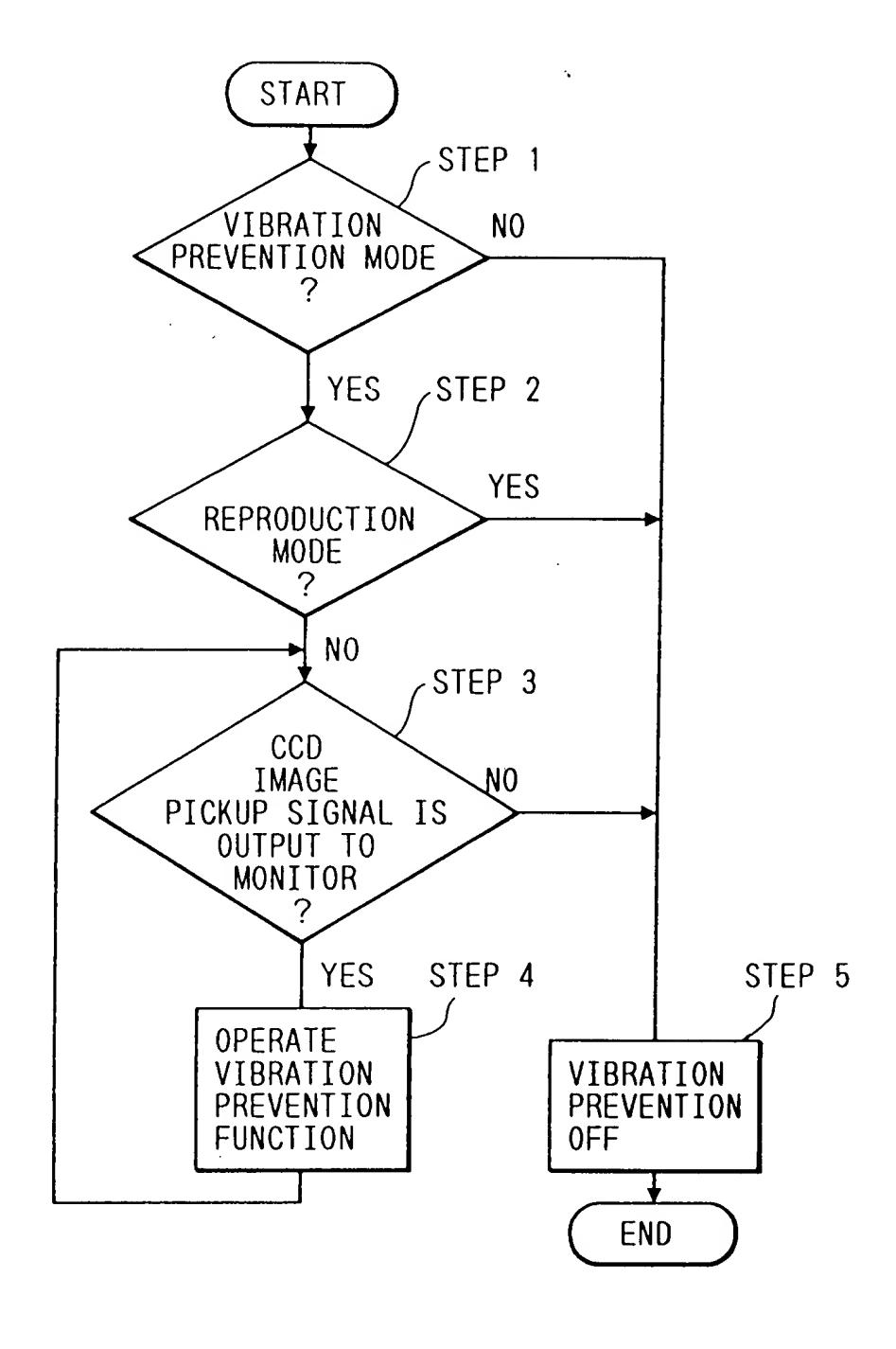


FIG. 3



F/G. 4

